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Grace Alicea

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: May 27, 2003

R. T. Bailis, et. al.

Serial No. 10/016,772

Group Art Unit: 2819

Filed: 10 December 2001

Examiner: Chang, Daniel

For: METHOD AND SYSTEM FOR USE OF AN EMBEDDED FIELD
PROGRAMMABLE GATE ARRAY INTERCONNECT FOR FLEXIBLE I/O
CONNECTIVITY

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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AMENDMENT

Sir:

In response to the Office Action dated 24 January 2003 (time for response extended one month by way of petition for extension herewith), please enter the following amendments and remarks in the present Application.

05/03/2003 MBERHE 00000031 10016772

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